

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1876	(716/1,18).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 14:55
L2	2524	(716/1,16,17,18).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 14:55
L3	46100	(programmable adj3 device) or (programmable adj chip) or fpga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 14:56
L4	60	(configur\$3 adj4 processor) and (configur\$3 adj3 peripheral) and parameter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 14:57
L5	6	3 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 14:57
S1	32761	(programmable adj3 device) or (programmable adj chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 14:55
S2	4390	parameter\$5 same configur\$3 same processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 11:47
S3	523	parameter\$5 same configur\$3 same peripheral	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 11:48

S4	353	S1 and S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 11:48
S5	20	S3 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 11:48
S6	50843	(programmable adj3 device) or (programmable adj chip) or fpga or pld	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 11:55
S7	138	S2 and S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 11:55
S8	23	S6 and S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/20 11:55